



INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	09/900393
				Filing Date	07/05/01
				First Named Inventor	Stephen Melvin Enrique Musoll Mario D. Nemirovsky Nandakumar Sampath
				Group Art Unit	Hassan Kizou
				Examiner Name	2662
Sheet	1	of	3	Attorney Docket Number	MIPS.0175-00-US

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ²			
mm	AA	4200927		Huges et al	04/29/1980	
mm	AB	5708814		Short et al	01/13/1998	
mm	AC	5724565		Dubey et al	03/03/1998	
mm	AD	5784649		Begur et al	07/21/1998	
mm	AE	6460105		Jones et al	10/1/2002	
mm	AF	6523109		Stephan G. Meier	02/18/2003	
mm	AG	6529515		Raz et al	03/04/2003	
mm	AH	6640248		Jacob W. Jorgensen	10/28/2003	
mm	AI	6625808		David R. Tarditi	09/23/2003	
mm	AJ	6738371		Lawrence Ayers	05/18/2004	
mm	AK	6738378		Tuck, III et al	05/18/2004	
mm	AL	6813268		Kalkunte et al	11/02/2004	
mm	AM	6535905		Kalafatis et al	03/18/2003	
mm	AN	20010004755		Levy et al	06/21/2001	
mm	AO	20010005253		Hiroshi Komatsu	06/28/2001	
mm	AP	20020083173		Musoll et al	06/27/2002	
mm	AQ	09/608750		Nemirovsky et al	Not published	

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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mm	BA	WO03005645 A1		Sampath et al	06/25/02	

Examiner Signature	Melvin Marcelo	Date Considered	6-17-2005
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		Examiner Name	Hasan Kizou		
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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
mm	CA	MELVIN ET AL, "Extended Instruction Set for a Packet Processing Applications," July 5, 2001, Disclosure Document #496559, USPTO	
mm	CB	MUSOLL ET AL, "Hardware Algorithm for Allocating and De-Allocating Consecutive Blocks of Memory," April 3, 2001, Disclosure Document #491557, USPTO	
mm	CC	MUSOLL ET AL, "Mechanism to Overflow Packets to a Software Controlled Memory When They Do Not Fit into a Hardware Controlled Memory," July 3, 2001, Disclosure Document #496391, USPTO	
mm	CD	MUSOLL ET AL., "Mechanism for Allowing a Limited Packet Head and/or Tail Growth Without Moving the Packet to a Different Memory Location," April 16, 2001, Disclosure Document #492429, USPTO	
mm	CE	MUSOLL ET AL., "Mechanism to Activate a Context When No Stream is Running in a Multi-Streaming Processing Core," April 16, 2001, Disclosure Document #492431, USPTO	
mm	CF	MUSOLL, ENRIQUE, "Functional Validation of a Packet Management Unit," May 18, 2001, Disclosure Document #494011, USPTO	
mm	CG	YAMAMOTO, WAYNE. <i>An Analysis of Multistreamed, Superscalar Processor Architectures</i> . University of California Santa Barbara Dissertation. December 1995. Santa Barbara, US.	
mm	CH	YAMAMOTO et al. "Increasing Superscalar Performance Through Multistreaming." <i>Parallel Architectures and Compilation Techniques (PACT '95)</i> . 1995	
mm	CI	The PowerPC Architecture: A Specification for a New Family of RISC Processors. 2 nd Ed. May 1994. pp. 70-72. Morgan Kaufmann. San Francisco, US.	
mm	CJ	MC68020 32-Bit Microprocessor User's Manual. 3 rd Ed.. 1989. pp. 3-125, 3-126, and 3-127. Prentice Hall, NJ, US.	
mm	CK	POTEL, M. J. "Real-Time Playback in Animation Systems." <i>Proceedings of the 4th Annual Conference on Computer Graphics and Interactive Techniques</i> . 1977. pp. 72-77. San Jose, CA, US.	
mm	CL	ARM Architecture Reference Manual. 1996. pp. 3-41, 3-42, 3-43, 3-67, and 3-68. Prentice Hall, NJ, US.	
mm	CM	ESA/390 Principles of Operation. IBM Online Publications Center Reference Number SA22-7201-08. Table of Contents and paras. 7.5.31 and 7.5.70. IBM Corporation. Boulder, CO, US.	
mm	CN	MC88110 Second Generation RISC Microprocessor User's Manual. 1991. pp. 10-66, 10-67, and 10-71. Motorola, Inc.	
mm	CO	DIEFENDORFF, KEITH ET AL. "Organization of the Motorola 88110 Superscalar RISC Microprocessor." <i>IEEE Journal of Microelectronics</i> . April 1992. pp. 40-63. Vol. 12, No. 2. IEEE. New York, NY, US.	
mm	CP	KANE, GERRY. <i>PA-RISC 2.0 Architecture</i> . 1996, pp. 7-106 and 7-107. Prentice Hall. NJ, US.	
Examiner Signature		Melvin Marcelo	Date Considered
			04-17-2005

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mm	CQ	DIEFENDORFF, KEITH ET AL. "Altivec Extension to PowerPC Accelerates Media Processing." <i>IEEE Journal of Microelectronics</i> . Vol. 20, No. 2 (2000): pp. 85-95			
mm	CR	GRUNEWALD, WINFRIED ET AL. "Towards Extremely Fast Context Switching in a Block Multithreaded Processor." <i>Proceedings of EUROMICRO 22</i> , 1996. pp. 592-599			
mm	CS	BRADFORD, JEFFREY ET AL. "Efficient Synchronization for Multithreaded Processors." <i>Workshop on Multithreaded Execution, Architecture, and Compilation</i> . Jan-Feb 1998. pp. 1-4			
mm	CT	PAI, VIJAY ET AL. "An Evaluation of Memory Consistency Models for Shared-Memory Systems with ILP Processors." <i>Proceedings of ASPLOS-VII</i> , Oct. 1996: pp. 12-23, ACM, Inc.			
mm	CU	YOAZ ET AL. "Speculation Techniques for Improving Load Related Instruction Scheduling." 1999. Pages 42-53, IEEE.			
mm	CV	KESSLER, R. E. "The Alpha 21264 Microprocessor: Out-of-Order Execution at 600 MHz." August 1998.			
mm	CW	DONALSON ET AL. "DISC: Dynamic Instruction Stream Computer, An Evaluation of Performance." 26 th Hawaii Conference on Systems Sciences. Vol. 1. 1993. Pages 448-456.			
mm	CX	NEMIROVSKY ET AL. "DISC: Dynamic Instruction Stream Computer." ACM. 1991. Pages 163-171			
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